09/649437

ABSTRACT OF THE DISCLOSURE

In one embodiment, a method for specifying addressability in a memory-mapped device is disclosed. A data access primitive is used to model

5   addressability for the memory-mapped device. Addressability comprises an address matching function, a lane matching function and one or more bus connections. A first starting address for the memory-mapped device is specified. A first set of addressing matching function, lane matching function and one or more bus connections for the memory-mapped device is generated

10   using the data access primitive and the first starting address.